

PATENT  
Docket No.: 29284/541

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: NARA, Yasuhiko et al.  
SERIAL NO. : 09/832,220  
FILED : April 11, 2001  
FOR : INSPECTION METHOD, APPARATUS AND SYSTEM FOR CIRCUIT PATTERN  
ART UNIT : 2881  
EXAMINER : Jack I. Berman

ASSISTANT COMMISSIONER FOR PATENTS  
Washington D.C. 20231

#8  
T. BELL  
3-7-02  
RECEIVED  
MAR - 1 2002  
TECHNOLOGY CENTER 2800

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. § 1.97(c)(2)**

S I R:

In conformance with the Applicants' duty of disclosure under 37 C.F.R. § 1.56(a), the references listed on the attached form PTO-1449 are hereby brought to the Examiner's attention. The Applicants' respectfully request that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear in the "references cited" on any patent to issue therefrom.

As required by 37 C.F.R. § 1.98, a copy of the references are submitted herewith.

As required by 37 C.F.R. § 1.97(c)(2), the Commissioner is authorized to charge the fee of \$180.00 specified in 37 CFR §1.17(p) to Kenyon & Kenyon Deposit Account No. 11-0600. The Commissioner is further authorized to charge any additional fees relevant to this filing to that same account. A duplicate of this sheet is attached for that purpose.

03/01/2002 NMOHMM1 00000074 110600 09832220  
02 FC:126 180.00 CH

Respectfully submitted,

  
John C. Altmiller

Registration Number 25,951

Dated: February 25, 2002  
KENYON & KENYON  
1500 K Street, N.W., Suite 700  
Washington, D.C. 20005-1257